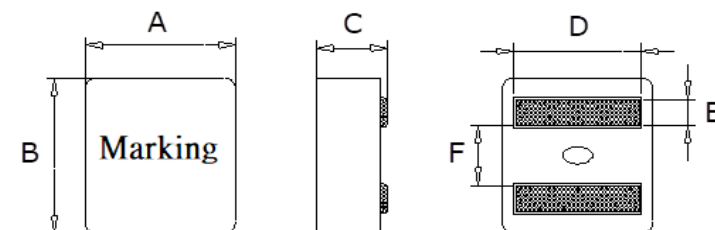


Features

- All test data is referenced to 25 °C ambient
- $I_{dc1}(I_{sat})$: will cause L_0 to drop 30% approximately.
- $I_{dc2}(I_{rms})$: will cause the coil temperature rise of 40°C approximately (Δt)
- Operating temperature range: - 55 °C to + 125 °C
- Storage temperature range: -55°C to +125°C.
- Tape and reel packaging: -55°C to +80°C



Part Number	Inductance (μH) $\pm 20\%$ @100KHz/0.1V	DC Resistance ($m\Omega$) Max.	$I_{dc1}(I_{sat})$ (A)		$I_{dc2}(I_{rms})$ (A)	$D \pm 0.3$
			Max.	Typ.	Typ.	
YMT0650A-R82M	0.82	4.18	20	24	21.0	5.3
YMT0650A-1R0M	1.0	4.52	18	23	20.0	5.3
YMT0650A-1R2M	1.2	5.83	16	22	18.0	5.3
YMT0650A-1R5M	1.5	6.30	14.5	19.5	17.0	5.3
YMT0650A-1R8M	1.8	7.10	13.5	18.5	16.0	5.3
YMT0650A-2R2M	2.2	8.50	12.0	16.0	13.0	5.2
YMT0650A-3R3M	3.3	12.5	10.0	12.5	11.0	5.2
YMT0650A-4R3M	4.3	16.2	8.5	11.0	9.0	5.2
YMT0650A-4R7M	4.7	18.4	8.0	10.5	8.5	5.2

A	B	C	D	E	F
6.6 ± 0.2	6.4 ± 0.2	4.8 ± 0.2	See Spec table	1.4 ± 0.3	2.6 ± 0.3

